DWC4R3600XXG
DDR4 8/16GB RGB
UDIMM Datasheet

DYNAC

Description

DYNAC's RGB memory module is organized 64 bits in a 288 pin memory module, Based on 8bit DDR4 FBGA components per module.
The LO-DIMM is intended for use in applications operating up to 1600MHz clock speeds and achieves high-speed data transfer rates of up to 3600 MHz.

Features

- DDR4 functionality and operations supported as definedin the component data sheet
- 288-pin, Unbuffered Daul In-Line memory module
- Fast data transfer rates: XMP 3600
- V_{DD} = 1.35V (typical)
- V_{PP} = 2.5V (typical)
- V_{DDSPD} = 2.2–3.6V
- Nominal and dynamic on-die termination (ODT) fordata, strobe, and mask signals
- Low-power auto self refresh (LPASR)
- Data bus inversion (DBI) for data bus
- On-die V_{REFDQ}generation and calibration
- On-board serial presence-detect (SPD) EEPROM
- 16 internal banks; 4 groups of 4 banks each
- Fixed burst chop (BC) of 4 and burst length (BL) of 8via the mode register set (MRS)
- Selectable BC4 or BL8 on-the-fly (OTF)
- Gold edge contacts
- Halogen-free
- Fly-by topology
- Terminated control, command, and address bus
- RGB light control software from major motherboard makers
- Intel XMP 2.0 accessible overclocking

Options

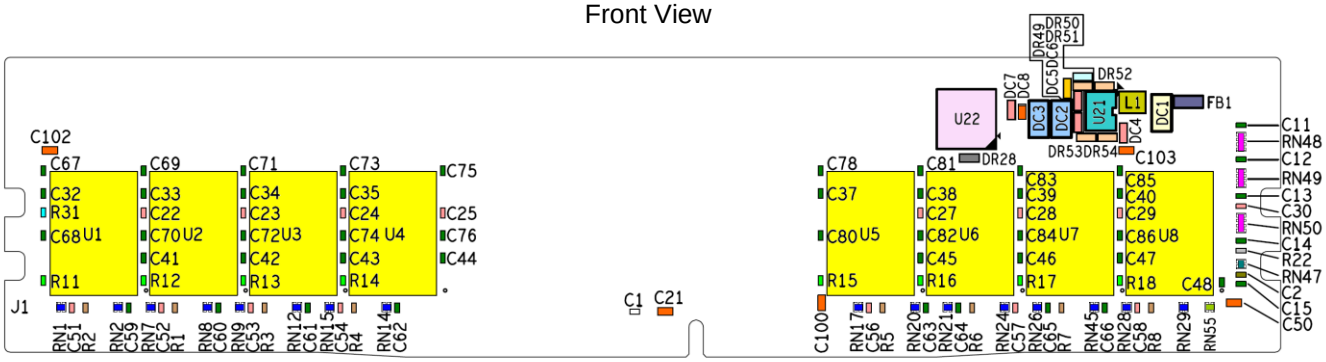
- Operating temperature
  - Commercial
    (0°C ≤ T_{OPER} ≤ +85°C)
- Package
  - 288-pin DIMM (halogen-free)
- Frequency/CAS latency
  XMP 3600@ CL18-22-22

Ordering Information

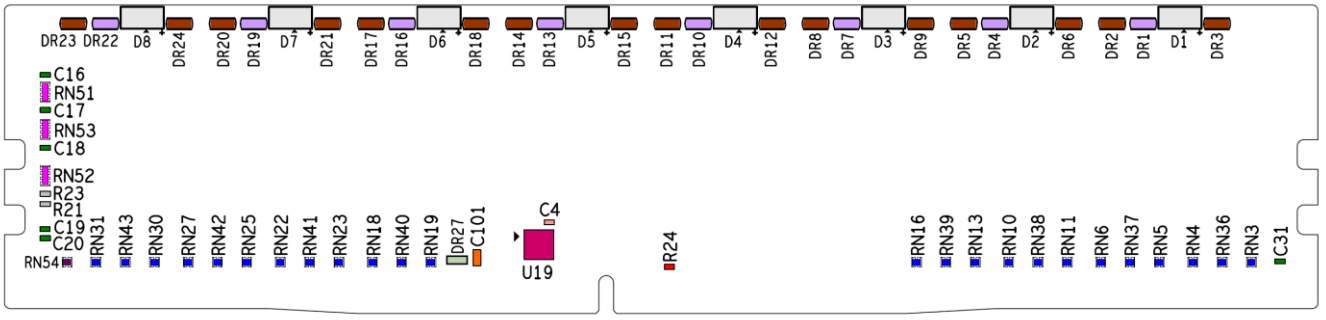
Density	Organization	Speed / Latency	Package
8GB	1Gx8 / One Rank	XMP 3600 (18-22-22)	288pin LO-DIMM
16GB	1Gx8 / 2 Rank	XMP 3600 (18-22-22)	288pin LO-DIMM

8GB 1Gx64(1 Rank by x8)

Front View

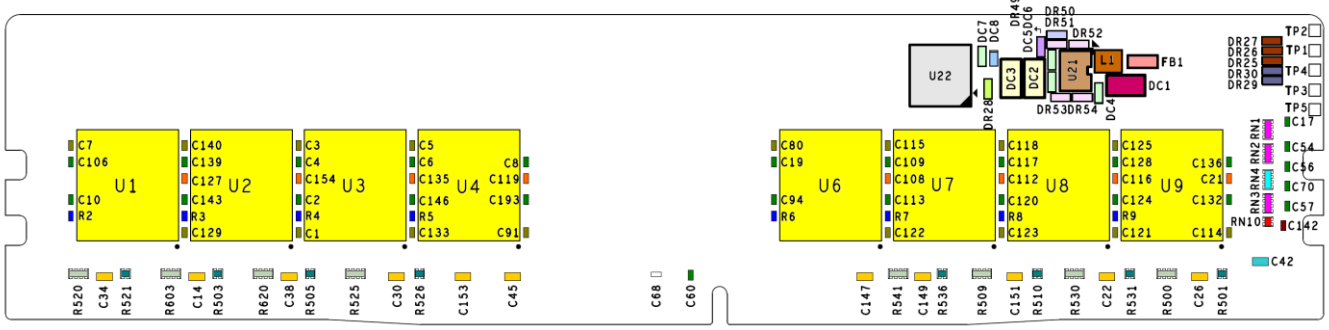


Back View

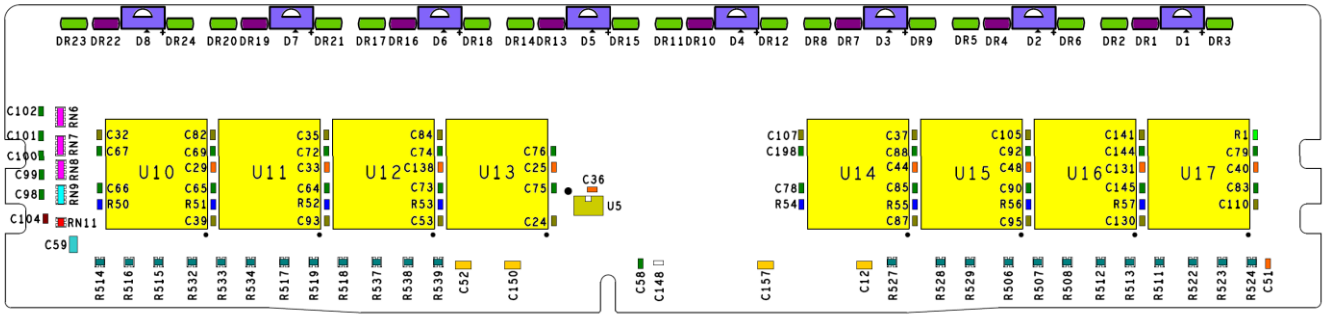


16GB 2Gx64(2 Rank by x8)

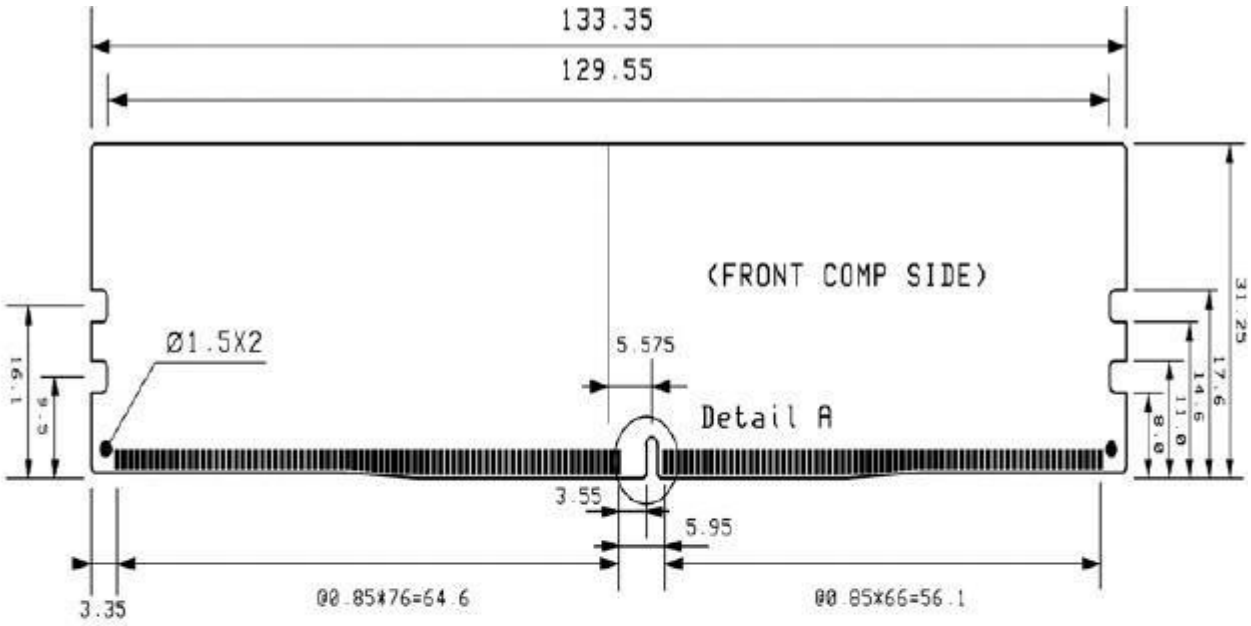
Front View

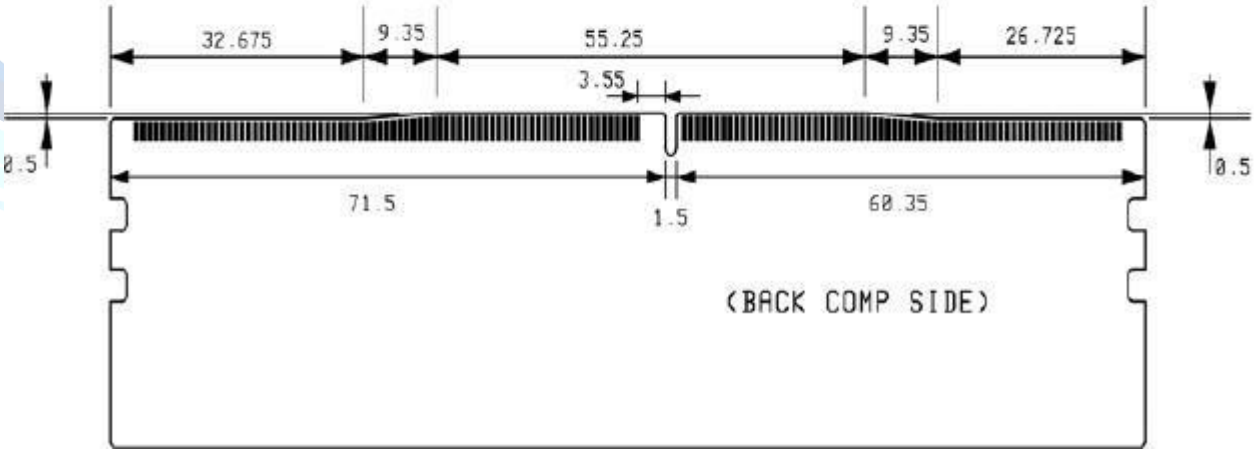


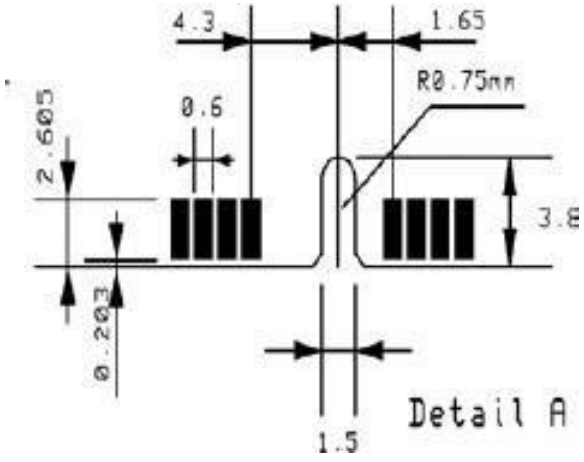
Back View

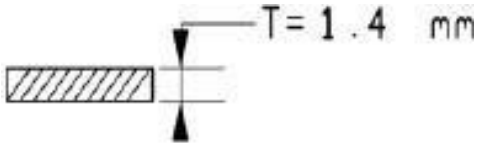


Module Dimensions









TOLERANCE: ±0.1mm
UNIT: mm 03/18/14


WITCHCRAFT
DDR4